

REMARKS

Claims 1-9, 11-21, 23-30 and 35-38 are pending in this application.

By this Amendment, claims 1, 2, 13 and 14 are amended to recite additional features disclosed in the specification at, for example, Fig. 3 and paragraph [0088] (see the clean copy of the substitute specification filed December 18, 2003).

Claims 3, 8, 9, 15, 20 and 21 are amended for clarity and consistency. See the specification at, for example, paragraph [0085]. Claims 10, 22 and 31-34 are canceled.

Claims 35-38 are added to recite additional features disclosed in the specification at, for example, paragraph [0088].

Reconsideration of the application is respectfully requested.

The Examiner is respectfully requested to consider the references submitted with the March 19, 2007 Information Disclosure Statement.

The Office Action rejects claims 1-34 under 35 U.S.C. §103(a) over Applicant's admitted prior art (AAPA) in view of U.S. Patent No. 6,362,798 to Kimura. This rejection is respectfully traversed.

Independent claims 1, 2, 13 and 14 are amended to recite additional features, as outlined above. In particular, claim 1 is amended to recite "sets a gate voltage of a voltage-rising transistor included in a transforming circuit to an initial voltage in order to turn on the voltage-rising transistor." Claim 2 is amended to recite "the transforming circuit comprising an initializing device that sets a gate voltage of the voltage-rising transistor included in the transforming circuit to an initial voltage in order to turn on the voltage-rising transistor."

Claims 13 and 14 are amended similarly. AAPA and Kimura do not disclose or suggest the additional features, as recited in claims 1, 2, 13 and 14.

In particular, AAPA does not disclose or suggest a transforming circuit or a voltage-rising transistor, much less setting an initial voltage in order to turn on the voltage-rising transistor.

Kimura discloses a compensating TFT 120. See Fig. 1. However, Kimura does not disclose or suggest setting an initial voltage in order to turn on this compensating TFT 120.

For at least the above reasons, AAPA and Kimura, either individually or in combination, do not disclose or suggest the subject matter recited in claims 1, 2, 13 and 14, and claims 3-9, 11, 12, 15-21 and 23-30 depending therefrom. Accordingly, withdrawal of the rejection of claims 1-9, 11-21 and 23-30 under 35 U.S.C. §103(a) is respectfully requested.

Claims 35-38 are each patentable at least in view of the patentability of claims 1, 2, 13 and 14, from which they respectively depend, as well as for additional features they recite. For example, AAPA and Kimura, either individually or in combination, do not disclose or suggest "the initial voltage being set by connecting the gate of the voltage-rising transistor to an initial set power source via a switch," as recited in claim 35, and similarly recited in claims 36-38.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims 1-9, 11-21, 23-30 and 35-38 are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



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Attachment:
Request for Continued Examination

Date: April 12, 2007

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